

<b>Notice of References Cited</b>	Application/Control No. 10/826,198		Applicant(s)/Patent Under Reexamination BREWER, SYMON	
	Examiner Linda Wong		Art Unit 2611	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,400,370	03-1995	Guo, Bin	375/371
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	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	"FPGA and CPLD Architectures: A Tutorial", Summer 1996, IEEE Design and Test of Computers, Vol. 13, No. 2, page 43, 51-53
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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